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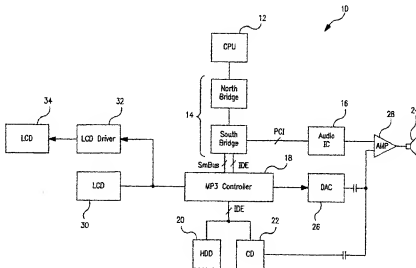
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(54) Title: AUDIO CONTROLLER FOR PORTABLE ELECTRONIC DEVICES



(57) Abstract: An audio controller (18) for use with laptop and notebook digital computers for reproducing compressed digital audio recordings. The controller (18) includes a drive interface (54) for traversing and accessing audio data files stored on a drive (20, 22) of a computer system (10). Function keys coupled to the controller (18) permit users to access drives (20, 22) containing desired audio data. The selected audio data is read from the drive (20, 22) into the controller (18). Decoding circuitry (56) decodes the audio data and generates a decoded audio data stream. The data stream can be converted to an analog signal by the controller (18), or sent to the audio subsystem of the computer system (10). Advantageously, the controller (18) operates when the computer system (10) is in an inactive state, for example in power saving mode or OFF, and operates in passthrough mode when the computer system (10) is ON or active.



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AUDIO CONTROLLER FOR PORTABLE ELECTRONIC DEVICES

BACKGROUND OF THE INVENTION

1. Field of the Invention

The present invention relates generally to portable devices for reproducing audio recordings, and more particularly, to a device for reproducing compressed digital audio recordings. Particular utility for the present application is in the reproduction of MP3 digital audio files, especially for use with portable computers, however other utilities are contemplated herein.

2. Description of Related Art

Presently there exist various portable devices for replaying digital audio recordings that have been compressed in accordance with a compressed audio digital recording format called MP3. These devices can be divided into two classes, those which store the MP3 compressed digital audio recordings in an electronic solid state memory, and those which record the compressed digital audio for subsequent reproduction using an electro-mechanical device such as a compact disk ("CD") player or on a hard disk drive of a digital computer.

Portable devices for replaying MP3 compressed digital audio recordings that use electronic solid state memory, i.e. flash-memory, are capable of storing about ten (10) music selections. With an add-in memory card, such devices can carry a total of about twenty (20) music selections. These MP3 players that store the MP3 compressed digital audio recordings in an electronic solid state memory consume comparatively little electrical power. Thus, such MP3 players provide an extended playing interval without battery replacement or recharging for the limited number of selections which they can store.

In addition to having a capacity for only a limited number of music selections, another characteristic of portable MP3 players that store compressed digital audio recordings in an electronic solid state memory is the inconvenience associated with loading the music selections into that memory. In general, such MP3 players require first downloading or obtaining copies of MP3 compressed digital audio recordings on a hard disk drive of a personal computer, and then transferring the MP3 compressed digital audio recordings from the personal computer to the portable MP3 player. The preceding operations are to be contrasted with the simplicity of merely inserting a

compact disk ("CD") into a CD player, or playing MP3 compressed digital audio recordings directly from a hard disk drive or CD drive of a digital computer.

MP3 players which preserve compressed digital audio recordings for reproduction using an electro-mechanical device are capable of storing many more music selections than portable MP3 players that store compressed digital audio recordings in an electronic solid state memory, e.g. hundreds or even more than one-thousand. However, usually MP3 players that use electro-mechanical devices require significant amounts of electrical power. Thus, portable players that reproduce music selections using an electro-mechanical device exhibit comparatively short playing interval, e.g. less than one (1.0) hour before batteries must be replaced or recharged.

Batteries used in laptop and notebook computers usually permit their operation for several hours before becoming discharged. As is readily apparent, a laptop or notebook computer can be to play MP3 compressed digital audio recordings using either the computer's CD-ROM or hard disk drive. Pending United States Patent Application Serial No. 09/136,207, now U.S. Patent No. _____, entitled "Low Power CD-ROM Player for Portable Computers" that was filed on August 19, 1998, which is hereby incorporated by reference in its entirety, describes how a conventional laptop or notebook computer, when simply playing a conventional music CD, consumes an unnecessarily large amount of electrical energy. Such an excessive electrical energy consumption drains a laptop or notebook computer's battery of power that is more prudently applied in performing microprocessor intensive tasks such as word processing and spreadsheet analysis. The solution presented in the '207 application is a state machine that operates when main power to the portable device is OFF. The '207 invention couples a CD-ROM to the audio subsystem (when main power is OFF) so that CDs can be played, without excessive battery drain, or without having to boot up the portable computer.

SUMMARY OF THE INVENTION

Accordingly, it is one object of the present invention to adapt laptop and notebook digital computers for reproducing compressed digital audio recordings when main power to the computer system is OFF. Another object of the present invention is to adapt laptop and notebook digital computers for storing MP3 compressed digital

1 audio recordings into a conventional portable MP3 player using as little energy as is
2 practicable.

3 In one embodiment, the present invention provides a computer system adapted
4 to play audio files which includes a computer subsystem comprising a system CPU
5 and a drive for storing audio data. The computer system also includes an audio
6 controller comprising a drive interface for selectively accessing the audio data from
7 the drive and memory for storing the audio data. Advantageously, the controller is
8 adapted to access, store and play the audio data when power is not being supplied to
9 said computer subsystem.

10 In another embodiment, the present invention provides a computer system
11 adapted to play audio data when said computer system is inactive, the computer
12 system including a computer subsystem comprising a system CPU and a drive for
13 storing audio data. An audio controller is provided comprising a drive interface for
14 selectively accessing the audio data from the drive and decoder circuitry for decoding
15 the audio data and generating a decoded signal. The controller is adapted to access
16 the drive to retrieve the audio data and decode the audio data when said computer
17 subsystem is inactive.

18 In method form, the present invention provides a method for playing audio
19 files in a computer system when said computer system is in an inactive state includes
20 the steps of activating an audio controller if a main CPU of a computer system is
21 inactive; selecting desired audio data; and generating an audio data stream from said
22 selected audio data.

23 It will be appreciated by those skilled in the art that although the following
24 Detailed Description will proceed with reference being made to preferred
25 embodiments and methods of use, the present invention is not intended to be limited
26 to these preferred embodiments and methods of use. Rather, the present invention is
27 of broad scope and is intended to be limited as only set forth in the accompanying
28 claims.

29 Other features and advantages of the present invention will become apparent
30 as the following Detailed Description proceeds, and upon reference to the Drawings,
31 wherein like numerals depict like parts, and wherein:

Brief Description of the Drawings

Figure 1 is an exemplary block diagram of a portable system in an ON state adapted to receive and play MP3 digital audio files, according to one embodiment of the present invention;

Figure 2 is an exemplary block diagram of a portable system in an OFF or inactive state adapted to receive and play MP3 digital audio files, according to one embodiment of the present invention;

Figure 3 is a more detailed system block diagram of the invention of Figures 1 and 2;

Figure 4 is a detailed block diagram of the MP3 audio controller of the invention of Figures 1 and 2;

Figure 5A is an exemplary block diagram of another embodiment of the present invention, depicting a portable system in an ON state, adapted to receive and play MP3 digital audio files, and utilizing an external MP3 decoding device; and

Figure 5B is an exemplary block diagram of another embodiment of the present invention, depicting a portable system in an OFF or inactive state adapted to receive and play MP3 digital audio files, and utilizing an external MP3 decoding device.

Detailed Description of Exemplary Embodiments

Figures 1-4 depict an example of the preferred MP3 audio controller of the present invention. As an overview, the present invention is directed to an MP3 audio controller 18 adapted to play stored MP3 files. It is intended, in this embodiment, that the MP3 controller of the present invention be integrated within a computer system 10 (e.g., portable lap-top computer) and is adapted with the necessary logic to permit selection, retrieval and play of MP3 files stored locally on the computer, without the necessity of having the computer system turned ON. As used herein, the term inactive is defined to mean a state in which main power is not being supplied (i.e., an OFF state), or when the system is in a sleep mode (such as may be defined under power management specifications). Thus, the present invention provides significant power savings when playing MP3 audio files.

Figure 1 depicts a computer system 10 adapted with the MP3 controller 18 of the present invention in an ON state. Generally, the computer system 10 includes a

1 system CPU 12, a Corelogic chipset 14, a hard disk drive (HDD) 20, a CD-ROM
2 drive (CD) 22, and an audio subsystem (denoted as "audio IC") 16 coupled to a
3 speaker system 24. When main power is being delivered to the system 10 (i.e., the
4 computer is ON), it is preferable that the MP3 controller does not control the play of
5 MP3 files, since such functionality is usually handled by the CPU 12 and an MP3
6 decoder (typically software). Thus, when the system is ON, the MP3 controller 18 is
7 transparent to commands between the drives 20 and/or 22 and the CPU. Although the
8 figures depict drives 20 and 22 as a hard disk device and a CD-ROM device,
9 respectively, it is intended that any drive mechanism (e.g., RAM drive, DVD drive,
10 backup drive, etc.) known to those skilled in the art can be substituted for these drives
11 20 and/or 22 without departing from the present invention.

12 Conversely, when the system is OFF, as depicted in Figure 2, the MP3
13 controller of the present invention operates to permit users to traverse the drives 20
14 and/or 22 to play MP3 files stored therein directly, without requiring that the CPU 12,
15 CPU chipset 14, or audio subsystem 16 be operating. To that end, as shown in this
16 Figure, system power need only be supplied to the controller 18, and to the drives 20,
17 22.

18 The system block diagram of Figure 3 represents a more detailed view of the
19 computer system 10, adapted with the MP3 controller of the present invention. As
20 shown, the CPU 12 and Corelogic chipset 14 (depicted as conventional "North
21 Bridge" and "South Bridge" I/O chipsets) communicate with controller 18, using both
22 an SMBus and an IDE bus. As is understood in the art, coupling of the controller 18
23 to an SMBus permits the user-programmability of the controller 18. The controller 18
24 also communicates with the drives 20 and/or 22 along the system IDE bus. As will be
25 described in more detail below, controller 18 can include an integrated audio DAC IC,
26 or be adapted to feed a decompressed MP3 file to an external audio DAC 26. The
27 external audio DAC 26 may be included as part of the integrated computer system 10
28 and/or a subset of the audio IC 16. In either event, the converted audio files are
29 amplified (at amplifier 28) to provide an audible signal to speaker system 24.
30 Additionally, as noted above, controller 18 is adapted to control drives 20, 22 to read
31 MP3 files therefrom. To that end, to permit users to traverse directory structures on
32 the drives, an external LCD display 30 is preferably provided. The LCD display

1 receives directory information from drives 20 and 22 (via controller 18) and displays
2 that information by file name/location. Likewise, the LCD display preferably
3 displays current status information of the controller 18, as will be described in greater
4 detail below. It should be noted that the use of LCD display 30 requires that
5 controller 18 be adapted with appropriate LCD display driver circuitry. It may be the
6 case, however, that the computer system 10 includes an LCD display 34 and LCD
7 driver circuitry 32, in which case, controller 18 may be coupled directly thereto.

8 Figure 4 depicts a detailed block diagram of the MP3 controller 18 of the
9 present invention. As an overview, controller 18 includes an internal processor 48,
10 memory 50 and 52, IDE bus interface 54, SMBus interface 42 and MP3 decoding
11 circuitry 56. The overall functionality of controller 18 is the ability to traverse drives
12 20, 22, permit users to choose a desired MP3 file, decompress the MP3 file and output
13 either a digital or analog signal (to be played by an external amplifier and speaker
14 system). Each of these components depicted in Figure 4 are described below.

15 Processor 48 is provided to control the general I/O functions, including access,
16 traversal and retrieval commands for drives 20 or 22. In the preferred embodiment,
17 external function keys 66 are provided to permit users to operate controller 18 and
18 drives 20 or 22 to play MP3 files. Function keys can include play, pause, fast
19 forward, rewind, next track, previous track, scan, etc. (or any combination thereof).
20 Since, in the preferred embodiment, the controller 18 of the present invention permits
21 traversal of directory structures and retrieval of files, it is also preferable to include
22 MENU and ENTER function keys 66. Controller 18 includes a function key interface
23 46 to interpret commands generated by function keys 66 and generate commands to
24 the processor 48. Instructions for retrieval and play of MP3 files are stored in flash
25 memory 52. These instructions are preferably user-programmable firmware,
26 permanently resident in memory 52. Upon activation of a function key, processor 48
27 receives instructions from memory 52. To communicate with drives containing MP3
28 data, a slave IDE interface 54 is provided. Upon user commands generated by the
29 function keys, processor 48 instructs slave IDE interface to control one of the drives
30 to begin traversing the directory structure. The directory structure in which MP3 files
31 are stored may be fixed (for example, a directory may be user-specified and stored in
32 flash memory 52), or the controller can permit users to traverse all directories and

1 files on the drive. Once a user has selected an MP3 file and wishes to play that file
2 (by pressing a play function key, for example), processor 48 instructs the slave IDE
3 interface 54 to retrieve that file from the drive. Preferably, to minimize disk activity
4 once a file selection is obtained, the file is transferred into RAM memory 50. It is
5 most preferable to include dual port SRAM 50, as shown, to store both the audio file
6 and to temporarily store instructions and/or program parameters used by the processor
7 48. Once the audio file is loaded into memory 50, the data is fed to MP3 decoder
8 circuitry 56.

9 Decoder circuitry 56 comprises a stream audio decoder 58, buffer memory 60
10 and either an internal audio DAC 62, or a DAC interface 64 for communicating with
11 an external audio DAC 26. Stream audio decoder 58 receives streaming audio data
12 from memory 50 and decodes the data according to a decoder algorithm stored
13 therein. Alternatively, a decoder algorithm may be stored in flash memory 52, loaded
14 into memory 50 upon activation of the controller, and supplied to the decoder 58.
15 Either way, it is preferable to permit users to update/modify the decoding algorithm.
16 Accordingly, it is preferable that memory 52 or decoder 58 stores an updatable
17 version of the decoder algorithm. In the preferred embodiment, decoder 58 is an MP3
18 audio file decoder. The output data generated by decoder 58 is decompressed digital
19 audio data, and may include standard digital audio formats like PCM format data.
20 The decoder outputs the decompressed data to a first in - first out (FIFO) buffer 60. If
21 controller 18 is adapted with an internal DAC, data from the buffer 60 is fed into the
22 DAC 60, which generates an analog audio signal, which in turn is fed to amplifier 28
23 and out to the speaker system (not shown). Alternately, if an external DAC is
24 available in the computer system 10 (for example, as part of the audio IC), the
25 decoder can include an appropriate interface 64. Interface 64 receives digital data
26 from memory 60 and communicates with an external DAC. In a similar fashion, the
27 external DAC 26 generates an analog signal which is supplied to the amplifier 28 and
28 speaker system.

29 As discussed briefly above, the controller preferably includes an SMBus
30 interface 42 to permit controller 18 to communicate with an SMBus of computer
31 system 10. The SMBus is provided for when the system is ON to pass along function
32 key commands to the system 14 and 12, and is also used to access the flash memory

52 of the controller 18 to permit upgrades and/or changes therein. Once commands are sent to the interface 46, said commands are communicated to the processor 48 for processing. It is also preferable that controller 18 include an LCD interface 57, which is coupled to the SMBus (via register block 44) and processor 48. In this way, the LCD interface 57 can generate signals indicative of both the users actions via function key interface 46, and the processor status. Processor status may include overall operation status (e.g., file loading, decompressing, file not found, etc.) and specific operational parameters (e.g., error status, component failure, etc.). Additionally, it is preferable to display the drive data, which may include directory tree structure, file name(s), etc. Additionally, MP3 files typically contain an ID tag that is descriptive of the title, song, etc. It is preferable that LCD interface 57 be adapted to read and display this tag data. Thus, LCD interface 57 is preferably adapted to display such drive data generate by processor 48.

Controller 18 includes an internal clocking mechanism 40 to clock the circuitry of the controller, and to communicate with timed devices (drives 20 or 22) over a timed bus (e.g., IDE bus). It will be understood by those skilled in the art that more than one clock frequency is typically required, for example, differing clocks supplied to processor 48, decoder 58 and audio DAC 62. The clock mechanism preferably includes a PLL timer that is clocked by a set crystal, as shown.

As described above, the controller 18 of the preferred embodiment operates to play compressed audio files when the system 10 is OFF. To that end, it is preferred that the controller 10 is activated by a user pressing one of the function keys (i.e., system power is supplied to controller 18 by pressing one of the function keys 66). Upon this event, power is coupled to the components of controller 18, and to the drive systems 20 and/or 22. By the same token, if the system 10 is ON, the controller of the present invention includes switches 68. Switches 68 operate to decouple the controller 18 from the IDE bus (as shown in Figure 3), thereby becoming transparent to the drives 20, 22 and the audio subsystem 16.

It should be noted that the controller 18 is preferably operable with both hard disk drives 20 and CD-ROM drives 22, either of which are conventional storage media for MP3 audio files. Accordingly, function keys 66 also preferably include

1 activation keys for the CD-ROM drive, which may include EJECT, FF/SCAN-FF,
2 RW/SCAN-RW, PLAY, PAUSE, STOP, MENU, ENTER etc.

3 Figures 5A and 5B depict another embodiment of the computer system 10' of
4 the present invention. Similar to the embodiment of Figures 1 and 2, the present
5 embodiment includes an MP3 controller 18' incorporated into a computer system 10'.
6 In this embodiment, however, the controller 18' is operable with an external MP3
7 player 70. Figure 5A depicts the system 10' when power is supplied to the system
8 components: CPU 12', Corelogic chipset 14', Audio IC 16' and drives 20' and/or
9 22'. When the system is on, MP3 audio files stored on either drive 20' or 22' can be
10 transferred to the external device 70. External MP3 players may include a CD player
11 72 for reading CDs having MP3 files stored thereon, and/or internal memory 74 for
12 temporary storage of MP3 files. Similar to the previous embodiment, controller 18'
13 preferable is transparent to system 10' when power is ON. In Figure 5B, the system
14 components are OFF or inactive. Controller 18' operates to decompress MP3 files
15 and send the decompressed data to external player 74. Alternatively, controller 18'
16 can operate to transmit the compressed data to the external player 74, where the data
17 is decompressed into an appropriate audio format by the player 74. It is preferable
18 that the external device 70 include conventional I/O interface (not shown) for
19 connection to controller 18' (via system 10'). For example, controller 18' and player
20 70 may include conventional RS232 (serial), USB, and/or TCP/IP communications to
21 exchange commands and transfer data therebetween. The decompressed files can be
22 stored in memory 74 of the external player 70.

23 Controller 18' includes similar components as the controller 18 of the previous
24 embodiment, except that it may not be necessary to include function keys 66 and
25 function key interface 46, since it is likely that portable player 70 includes such
26 functionality. Similarly, it may not be necessary to include display functionality with
27 controller 18' if portable player 70 is equipped with an appropriate display to view
28 drive directory structures and files.

29 Thus, it is evident that there has been disclosed an audio controller for portable
30 electronic devices that satisfies the aims and objectives stated herein. Those skilled in
31 the art will recognize numerous modifications that may be made to the present
32 invention. For example, although the controller 18 and 18' of the present invention

1 has been described with reference to MP3 audio data, it should be readily apparent
2 that the controller 18 and 18' is independent of the specific format of audio data, and
3 should instead be viewed as a general-purpose audio controller capable of receiving,
4 playing, and/or decompressing any type of audio data, not limited to MP3 format data.

5 Other modifications are possible. For example, the controller 18 of Figures 3
6 and 4 is depicted and described as being coupled (or decoupled) to an IDE bus, those
7 skilled in the art will recognize that the controller can likewise include other bus
8 interface technologies, depending on the bus configuration of system 10. Thus, for
9 example, controller 18 may be modified to control SCSI drives, and include an SCSI
10 interface for exchanging commands and data according to SCSI protocols. Likewise,
11 it may be desirable to adapt controller 18 with conventional network protocols (e.g.,
12 TCP/IP, etc.) for communication with remote systems (not shown) in a conventional
13 network.

14 Still further modifications are possible. The controller 18 of the present
15 invention has been described herein as including decoding circuitry 56 to decode
16 audio data when the system 10 is OFF. However, it is contemplated that audio files,
17 such as MP3 files could be decoded and stored in a decoded format on the drives 20
18 and/or 22, for example when the system 10 is ON. If decoded (decompressed) is
19 accessed by the controller 18, this data is stored into memory 50 and supplied directly
20 to audio DAC 62 or audio DAC interface 64. In other words, no decoding is
21 necessary for such data and controller 18 plays the decoded data directly. Those
22 skilled in the art will recognize numerous additional modifications, and all such
23 modifications are deemed within the spirit and scope of the present invention, only as
24 limited by the appended claims.

CLAIMS

1. A computer system adapted to play audio files, said computer system comprising:
 - a computer subsystem comprising a system CPU and a drive for storing audio data; and
 - an audio controller comprising a drive interface for selectively accessing said audio data from said drive and memory for storing said audio data, said controller being adapted to access, store and play said audio data when said computer subsystem is in an inactive state.
2. A computer system as claimed in claim 1, said audio controller further comprising decoder circuitry receiving said audio data and outputting a decompressed stream of audio data.
3. A computer system as claimed in claim 2, said decoder circuitry further comprising a digital to analog circuit receiving said decompressed audio data stream and generating an analog audio signal.
4. A computer system as claimed in claim 2, said decoder circuitry further comprising a buffer memory for temporary storage of said decompressed audio data stream.
5. A computer system as claimed in claim 4, wherein said buffer memory comprises a first-in-first-out (FIFO) memory.
6. A computer system as claimed in claim 2, said controller further comprising interface circuitry to interface said stream of audio data with an external digital-to-analog converter circuit, and said computer subsystem further comprises said external digital-to-analog converter circuit for receiving said decompressed data stream from said interface.
7. A computer system as claimed in claim 1, said controller further comprising an LCD interface 57 for generating signals to an LCD display for displaying directory and file information data associated with said drive.
8. A computer system as claimed in claim 1, said controller further comprising a function key interface operable with a plurality of function keys, said function keys generating user commands to said controller through said function key interface.

1 9. A computer system as claimed in claim 2, said controller further comprising a
2 processor for controlling the operation said drive and said decoder circuitry.

3 10. A computer system as claimed in claim 9, wherein said controller further
4 comprises a flash memory for storing data and commands for use by said processor
5 for controlling said drive and said decoder circuitry.

6 11. A computer system as claimed in claim 1, said controller further comprising
7 an SMBus interface to exchange commands and data along an SMBus.

8 12. A computer system as claimed in claim 1, said audio data on said drive being
9 stored as a file in a directory, said controller being further adapted to permit users to
10 traverse said drive and select desired directory and file.

11 13. A computer system as claimed in claim 1, said audio data further comprising
12 tag data indicative of a title, and said controller further comprising a display interface
13 for displaying said tag data upon access of said audio data by said controller.

14 14. A computer system as claimed in claim 1, said controller further comprising a
15 switch for switching said controller to an inactive state when said power is supplied to
16 said computer subsystem, and for switching said controller to an active state when
17 said power is not being supplied to said computer subsystem.

18 15. A computer system as claimed in claim 1, said drive comprising a hard disk
19 drive or a CD-ROM drive being adapted to operate of an IDE bus.

20 16. A computer system as claimed in claim 1, said drive comprising an IDE drive
21 and said drive interface comprising an IDE drive interface for exchanging commands
22 and data between said controller and said drive.

23 17. A computer system as claimed in claim 2, wherein said audio data comprising
24 MP3 audio data, and said decoder circuitry comprising an MP3 audio data decoder.

25 18. A computer system adapted to play audio data when said computer system is
26 in an inactive state, comprising:

27 a computer subsystem comprising a system CPU and a drive for storing audio
28 data; and

29 an audio controller comprising a drive interface for selectively accessing said
30 audio data from said drive and decoder circuitry for decoding said audio data and
31 generating decoded audio data, said controller being adapted to access said drive to

1 retrieve said audio data and decode said audio data when said computer subsystem is
2 in an inactive state.

3 19. A computer system as claimed in claim 18, said decoder circuitry further
4 comprising a digital to analog circuit receiving said decoded audio data and
5 generating an analog audio signal.

6 20. A computer system as claimed in claim 19, said decoder circuitry further
7 comprising a buffer memory for temporary storage of said decoded audio data.

8 21. A computer system as claimed in claim 2, said controller further comprising a
9 digital-to-analog interface to interface said decoded audio data with an external
10 digital-to-analog converter circuit, and said computer subsystem further comprises
11 said external digital-to-analog converter circuit for receiving said decoded data from
12 said interface.

13 22. A computer system as claimed in claim 18, said controller further comprising
14 an LCD interface 57 for generating signals to an LCD display for displaying directory
15 and file information data associated with said drive.

16 23. A computer system as claimed in claim 18, said controller further comprising
17 a function key interface operable with a plurality of function keys, said function keys
18 generating user commands to said controller through said function key interface.

19 24. A computer system as claimed in claim 23, said controller further comprising
20 a processor for controlling the operation said drive and said decoder circuitry.

21 25. A computer system as claimed in claim 24, wherein said controller further
22 comprises a flash memory for storing data and commands for use by said processor
23 for controlling said drive and said decoder circuitry, and wherein said commands and
24 data being supplied to said processor upon activation of one of said function keys.

25 26. A computer system as claimed in claim 18, said controller further comprising
26 an SMBus interface to exchange commands and data along an SMBus.

27 27. A computer system as claimed in claim 18, said audio data on said drive being
28 stored as a file in a directory, said controller being further adapted to permit users to
29 traverse said drive and select desired directory and file.

30 28. A computer system as claimed in claim 18, said audio data further comprising
31 tag data indicative of a title, and said controller further comprising a display interface
32 for displaying said tag data upon access of said audio data by said controller.

1 29. A computer system as claimed in claim 18, said controller further comprising
2 a switch for switching said controller to an inactive state when said power is supplied
3 to said computer subsystem, and for switching said controller to an active state when
4 said power is not being supplied to said computer subsystem.

5 30. A computer system as claimed in claim 18, said drive comprising a hard disk
6 drive or a CD-ROM drive being adapted to operate of an IDE bus.

7 31. A computer system as claimed in claim 18, said drive comprising an IDE drive
8 and said drive interface comprising an IDE drive interface for exchanging commands
9 and data between said controller and said drive.

10 32. A computer system as claimed in claim 18, wherein said audio data
11 comprising MP3 audio data files, and said decoder circuitry comprising an MP3 audio
12 data decoder.

13 33. A computer system as claimed in claim 18, further comprising memory for
14 storing said audio data.

15 34. A method for playing audio files in a computer system when said computer
16 system is in an inactive state, comprising the steps of:

17 activating an audio controller if a main CPU of a computer system is in an
18 inactive state;

19 selecting desired audio data; and

20 generating an audio data stream from said selected audio data.

21 35. A method as claimed in claim 34, further comprising the step of:
22 decoding said selected audio data and generating a decoded audio data stream.

23 36. A method as claimed in claim 34, further comprising the step of:
24 generating an analog audio signal from said audio data stream.

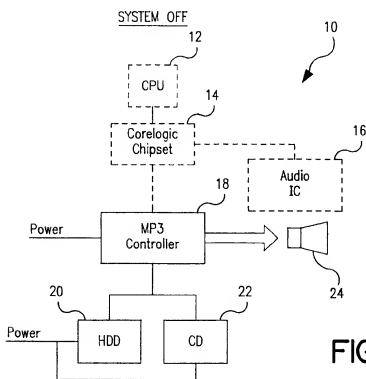
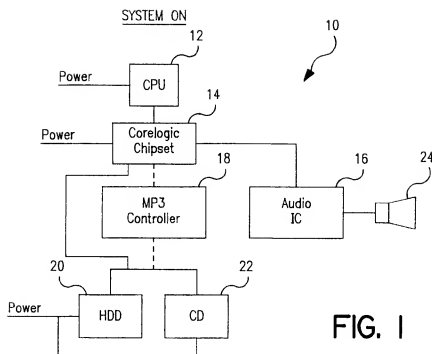
25 37. A method as claimed in claim 34, further comprising the step of:
26 storing said selected audio data in memory associated with said audio
27 controller.

28 38. A method as claimed in claim 34, further comprising the steps of:
29 controlling a drive of said computer system to access said audio data; and
30 controlling said drive to retrieve said audio data.

31 39. A method as claimed in claim 38, further comprising the step of:
32 traversing said drive to locate said desired audio data.

- 1 40. A method as claimed in claim 34, further comprising the step of:
2 displaying information related to said audio data.
- 3 41. A method as claimed in claim 34, further comprising the step of:
4 coupling said controller to said CPU through an SMBus.

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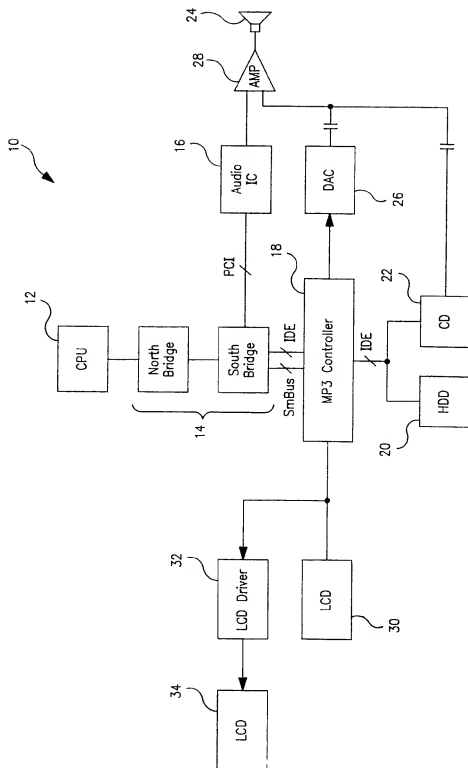


FIG. 3

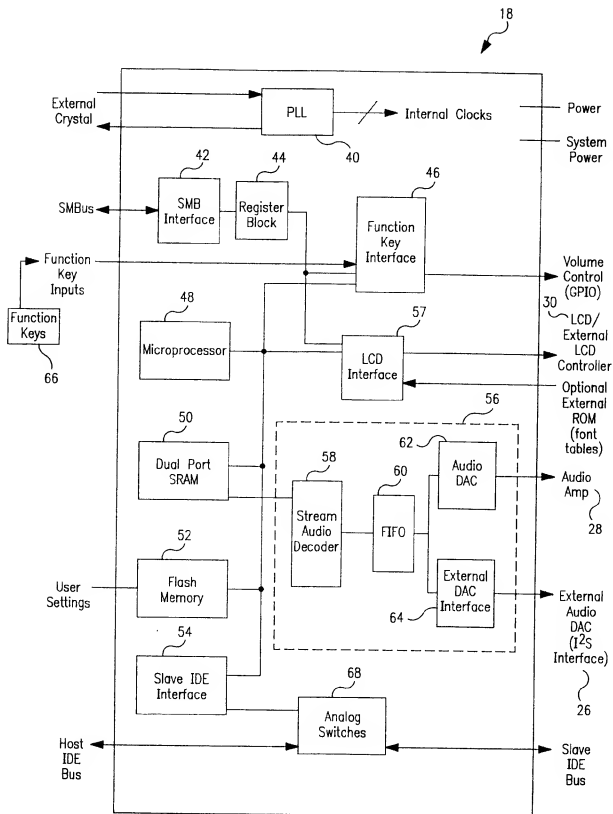
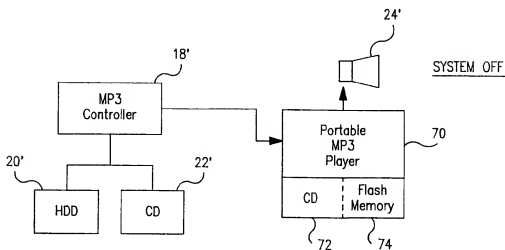
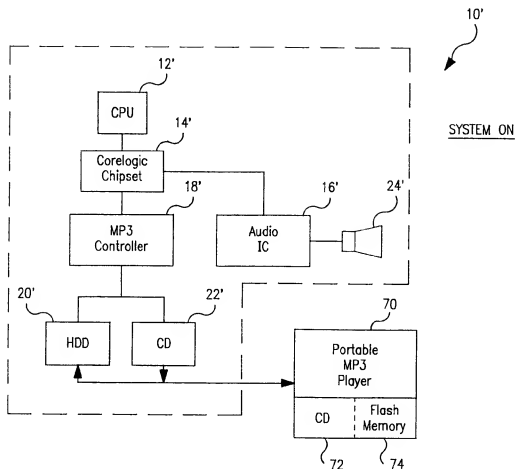


FIG. 4



INTERNATIONAL SEARCH REPORT

International application No.
PCT/US01/04628

A. CLASSIFICATION OF SUBJECT MATTER

IPC(7) : G06F 1/32; G11B 20/10

US CL : 713/300, 310, 323, 324; 710/14, 74

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 713/300, 310, 323, 324; 710/14, 74

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

WEST

search terms: computer, audio, inactive, inoperate, disable, power, play, portable

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	EP 0 945 778 A2 (CHAN et al) 29 September 1999, abstract, fig. 1,	1-41
Y	KR 99078492A (LEE) 05 November, 1999, col. 1, line 7-col. 3, line 14, figs. 1-3	1-41

☐ Further documents are listed in the continuation of Box C. ☐ See patent family annex.

* Special categories of cited documents:	* later documents published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
A document defining the general state of the art which is not considered to be of particular relevance	*X* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
E earlier document published on or after the international filing date	*Y* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
L document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	*Z* document member of the same patent family
O document referring to an oral disclosure, use, exhibition or other means	
P document published prior to the international filing date but later than the priority date claimed	

Date of the actual completion of the international search 17 MAY 2001	Date of mailing of the international search report 14 JUN 2001
Name and mailing address of the ISA/US Commissioner of Patents and Trademarks Box PCT Washington, D.C. 20231 Facsimile No. (703) 305-3230	Authorized officer THOMAS C. LEE <i>James R. Matthews</i> Telephone No (703) 305-9717